

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Gray *et al.* Conf. No.: 9703
Serial No.: 10/065,837 Art Unit: 2822
Filed: 11/25/2002 Docket: BUR920010190 (TBMB-0018)
Title: HIGH PERFORMANCE EXAMINER: Lewis, M.
VERTICAL PNP TRANSISTOR
AND METHOD

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COMMISSIONER FOR PATENTS

DESTINATION FACSIMILE NUMBER: 703-872-9306

Transmitted herewith is: Amendment in 8 pages
in the above identified application.

- ☒ No additional fee is required.
- ☒ The Commissioner is hereby authorized to charge and credit Deposit Account No. 09-0456 (IBM) as described below.
- ☒ Credit any overpayment.
- ☒ Charge any additional fee required.




Spencer K. Warnick, Reg. No. 40,398

DATE: November 23, 2004

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on the date shown below.

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VERTICAL PNP TRANSISTOR
AND METHOD**Examiner:** Lewis, M.Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**RECEIVED
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Sir:

I. INTRODUCTORY COMMENTS:

In response to the Office Action of August 25, 2004, please amend the above-referenced patent application as follows: